



aw.
1-4-96

14/C

500.26967R00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: K. KATSURA, et al.
Serial No.: 07/985,141
Filing Date: December 3, 1992
For: GRAPHIC PROCESSING APPARATUS
UTILIZING IMPROVED DATA TRANSFER
TO REDUCE MEMORY SIZE
Group: 2301
Examiner: M. Zimmerman



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1-4-96

AMENDMENT

Honorable Commissioner of Patents
and Trademarks
Washington, DC 20231

December 22, 1995

Sir:

The following amendments and remarks are being
submitted in response to the Communication from the Examiner
dated November 24, 1995.

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GROUP 240

IN THE CLAIMS:

Please amend the claims as follows:

1. (amended) A graphic processing apparatus
comprising:
memory means, including a plurality of memory
locations in an array of columns, having corresponding
column addresses and rows, having corresponding row
addresses, for storing data
data processing means for specifying a row address
in said memory means for retrieval of data from the memory
locations at the different column addresses within the